DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

P.O. Address: Nishi 3-2-62 Kunitachi-shi, Tokyo, Japan 186-0005

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled INTEGRATED CIRCUITS HAVING POST-SILICON ADJUSTMENT CONTROL the specification of which

(check one) X is attached	l hereto.		
was	filed on	as	
	Application Serial No		
	and was amended on	, if	applicable.
	eviewed and understand the amendment referred to abo	contents of the above identified sove.	specification, including the
I acknowledge the duty to Federal Regulations §1.56		is material to patentability as defi	ined in Title 37, Code of
patent or inventor's certific	cate listed below and have a	United States Code, §119 of any lso identified below any foreign a f the application on which priority	application for patent or
Prior Foreign Application	(s)		Priority Claimed
			Yes No_X
(Number)	(Country)	(Day/Month/Year Filed)	
application in the manner p duty to disclose material in between the filing date of	provided by the first paragra formation as defined in Tit the prior application and the	of this application is not disclose aph of Title 35, United States Codle 37, Code of Federal Regulation antional or PCT international filipending (Status-patented, pending, 2	le, §112, I acknowledge the as, §1.56(a) which occurred ing date of this application:
(Application Serial No.)	(Filing Date)	(Status-patented, pending,	abandoned)
I hereby appoint the followi Trademark Office connection Richard D. Fuerle, Registrat	on therewith:	application and to transact all busin	less in the Patent and
Address all telephone calls t	to: Richard D. Fuerle at telep	phone no. <u>(716) 774-0091</u>	
Address all correspondence		Richard D. Fuerle	
		1711 West River Road	
	•	Grand Island, NY 14072	
tion and belief are believed a statements and the like so m	to be true; and further that the ade are punishable by fine or	vn knowledge are true and that all sees statements were made with the imprisonment, or both, under Sectimay jeopardize the validity of the a	knowledge that willful false ion 1001 of Title 18 of the
Full name of sole inventor: Inventor's Signature Residence (City, State), Tol	<u> </u>	A Date: December	3rd, 2003
RESIDENCE LLIEV STREET A			